

Danny Rittman



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OBJECTIVES

Senior management with leadership responsibilities to achieve corporate's goals.

EXPERIENCE

CTO | GBT Technologies, Santa Ana, CA

3/2012 – Current

Leading the company's technological direction according to market needs, focusing on AI in the medical, communication, IoT, image/pattern recognition, and cybersecurity domains. In charge of AI hardware and software teams for a complete system's development. R&D AI technologies are in the domains of big data (data mining, data storage, data sharing, and data visualization), ML based Computer Vision, Face/Image recognition/analysis and cybersecurity, cognitive algorithms. Incorporating Deep Learning algorithms into EDA (Electronic Design Automation) software tools to accelerate Digital, Analog, RF integrated circuits design process, significantly enhancing advanced nanometer chip's design process and shortening time-to-market factor. Drive and manage pre-sale campaigns working closely with customers (Management, Engineering) to build and execute work plans for campaign success. Perform initial qualification of opportunity, involving AE/R&D as needed for supporting technical matters. Share and execute the account strategy in a Matrix organization with the sales, FAE & R&D teams, both locally and globally.

Working closely with a team of product line managers to shape, refine and execute our embedded software business strategy. Managing all aspects of marketing across numerous product lines, including pricing, packaging, positioning and promotion. Own product architecture, initiating the design and

leading teams for next product development to ensure future revenue stream. Development and implementation of business plans promoting corporate sales plans and goals.

Maintain and develop customer relationships with key stakeholders, influencers and decision makers. Technical papers writing, tradeshows presentations/lectures and visiting worldwide customers to support corporate technology marketing. Working with sales, application engineers, technical marketing and partners to provide a broad outlook of corporate technology, tools and concepts for domestic/international marketing purposes.

CTO/Member of the board | Micrologic Design Automation
9/2007 – 3/2012

Leading the company's technological direction according to market needs with EDA software tools. Implementing AI image processing techniques into corporate tools using R programming, Data lakes and predictive analytics, targeting integrated circuits design productivity enhancements during planning and designing Digital, Analog, RF and AMS backend design. Drive and manage pre-sale campaigns working closely with customers (Management, Engineering) to build and execute work plans for campaign success. Perform initial qualification of opportunity, involving AE/R&D as needed for supporting technical matters. Share and execute the account strategy in a Matrix organization with the sales, FAE & R&D teams, both locally and globally.

Senior Automation Manager | IBM
3/2002 – 6/2007

Managing internal development of IBM proprietary, AI microchips in a wide variety of fields using EDA software automation tools (Digital, Analog and RF). Leading technological/Marketing direction for productivity enhancements during planning and design stages. Using IBM's internal EDA tools and technology to architect and automate AI microprocessor's hardware and software design. Working closely with a team of product design managers to shape, refine and execute embedded product's business strategy. Own product's architecture, initiating the design and leading teams for next product development to ensure project's success. Development and implementation of product's business plans, promoting corporate sales plans and goals.

RV CAD Manager | Intel Corporation

3/1998 – 6/2002

In charge of RV (Reliability Verification) CAD support services. Working on Intel design types. In charge of complete chip's RV analysis and related CAD support. Working with Cadence's and Intel's IC layout internal tools for layout design purposes. Managing a team of physical designers/CAD programmers. Manager and individual contributor as part of the team. Supporting reliability verification flow for Electro migration and Self-heat analysis. Developing & supporting local scripts (SKILL, Perl, TCL) and programs to enhance work productivity.

Leading R&D teams developing Low-Power Application Processors and Chip-sets for the handheld/mobile markets. Managing software development teams, across the corporation structure for Intel's integrated circuits reliability and performance. Technical papers writing, tradeshow presentations/lectures and visiting worldwide customers to support corporate technology marketing. Overall ownership for developing the entire product portfolio, manufacturing and operations.

EDUCATION

Colorado Technical University, CO

MSc – 2020 – Computer Science – AI and ML technology

La Salle University, LA

MSc/PhD – 1998 – Computer Science – Advanced IC Design algorithms

University of Bridgeport, CT

BSc – 1992 – Electrical Engineering – VLSI Design

Technion Israel Institute of Technology, Israel

AA – 1982 – Computer Engineering – VLSI Design

[Currently attending DCS (Doctoral of Computer Science) online program at Colorado Technical University – Concentration: AI Technology. Expected Graduation: Oct 2023]

SKILLS

- Proficient knowledge and experience of Integrated Circuits design software tools and technologies. Cadence, Mentor Graphics, Synopsys and more.
- Experience with technical knowledge of architecture and programming languages C, C++, C# and similar.
- Vast experience with scripting languages Python, Cadence SKILL, PERL, TCL, Shell.
- Expert user of MS Office Tools as part of daily work. (Presentations, Technical/Marketing papers, Project's planning, Etc.)

GENERAL

- Israeli, US Citizen
- References: Upon Request

